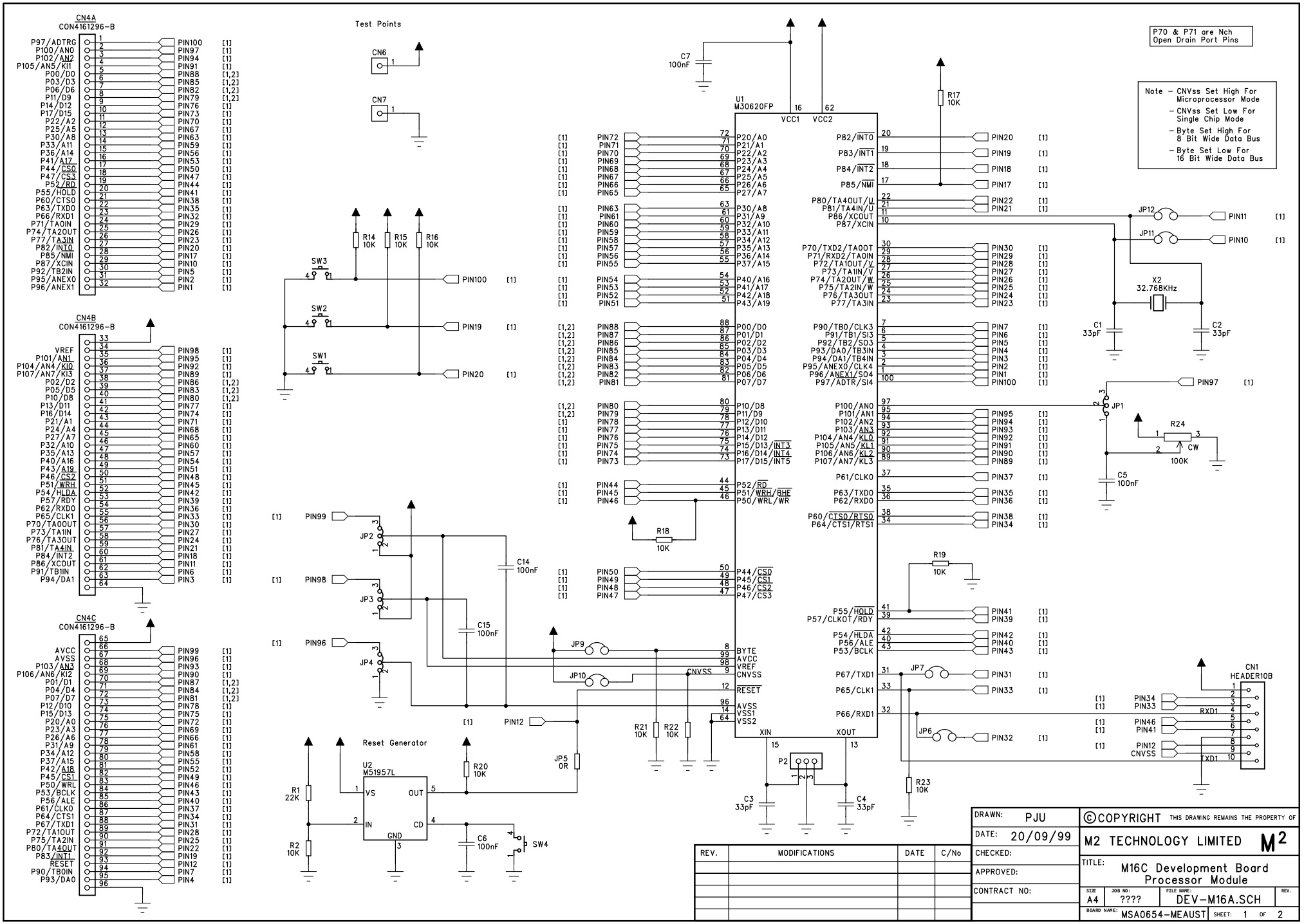
CN4A
CON4161296-B
CN4B
CON4161296-B
CN4C
CON4161296-B
Test Points
CN6
CN7
U1
M30620FP
VCC1
VCC2
P70 & P71 are Nch
Open Drain Port Pins
Note – CNVss Set High For
Microprocessor Mode
– CNVss Set Low For
Single Chip Mode
– Byte Set High For
8 Bit Wide Data Bus
– Byte Set Low For
16 Bit Wide Data Bus
X2
32.768KHz
C1
33pF
C2
33pF
C5
100nF
C7
100nF
C14
100nF
C15
100nF
C3
33pF
C4
33pF
C6
100nF
R14
10K
R15
10K
R16
10K
R17
10K
R18
10K
R19
10K
R20
10K
R21
10K
R22
10K
R23
10K
R24
100K
CW
R1
22K
R2
10K
SW1
SW2
SW3
SW4
JP1
JP2
JP3
JP4
JP5
0R
JP6
JP7
JP9
JP10
JP11
JP12
P2
CN1
HEADER10B
RXD1
TXD1
Reset Generator
U2
M51957L
VS
IN
GND
OUT
CD
XIN
XOUT
RESET
BYTE
AVCC
VREF
CNVSS
AVSS
VSS1
VSS2
REV.
MODIFICATIONS
DATE
C/No
DRAWN: PJU
DATE: 20/09/99
CHECKED:
APPROVED:
CONTRACT NO:
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M2 TECHNOLOGY LIMITED
TITLE: M16C Development Board
Processor Module
SIZE A4
JOB NO: ????
FILE NAME: DEV-M16A.SCH
REV.
BOARD NAME: MSA0654-MEAUST
SHEET: 1 OF 2